#### COMP 122/L Lecture 12

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#### Outline

- Memory instructions
  - Load (ldr)
  - Store (str)
- Arrays

## Memory Operations

#### Refresher

You've already seen one form of ldr for handling strings

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```
.data
my_string:
    .asciz "hello"

.text
ldr r0, =my string
```

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You've already seen one form of 1dr for handling strings

```
.data
my_string:
    .asciz "hello"

    .text
    ldr r0, =my_string

Puts starting address of "hello" in r0
```

<sup>-</sup>This was used for setting up swi instructions

<sup>-</sup>While this uses memory, it did so indirectly (the swi instruction actually read the memory, not us)

### Putting Integers in Memory

.word directive will put a 32-bit integer in memory, much like .asciz will put a string in memory

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.word directive will put a 32-bit integer in memory, much like .asciz will put a string in memory

```
.data
my_string:
    .asciz "hello"
first_int:
    .word 42
second_int:
    .word 38
```

Step I: use ldr to put its address into a register...

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```
.data
first_int:
   .word 42
second_int:
   .word 38

.text
ldr r0, =first_int
```

Step 2: use ldr with [] to read the value at the address

```
.data
first_int:
   .word 42
second_int:
   .word 38

.text
ldr r0, =first_int
```

Step 2: use ldr with [] to read the value at the address

```
.data
first_int:
   .word 42
second_int:
   .word 38

.text
ldr r0, =first_int
ldr r1, [r0]
```

Step 2: use ldr with [] to read the value at the address

```
.data
first int:
  .word 42
second int:
  .word 38
  .text
ldr r0, =first int
ldr r1, [r0]
```

Step I: use ldr to put its address into a register...

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```
.data
first_int:
   .word 42
second_int:
   .word 38

.text
ldr r0, =first_int
```

Step 2: use str to write a value at that address

```
.data
first_int:
   .word 42
second_int:
   .word 38

.text
ldr r0, =first_int
```

Step 2: use str to write a value at that address

.data

#### Example:

memory\_variables.s

## Arrays

#### Specifying Arrays

Only distinction from variables: multiple values are specified with the .word directive

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```
.data
first_int:
.word 42
```

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Only distinction from variables: multiple values are specified with the .word directive

```
.data
first_int:
   .word 42
array:
   .word 32, 65, 76, 87
```

```
.data
arr:
  .word 32, 65, 76
  .text
ldr r0, = arr
ldr r1, [r0]
add r0, r0, #4
ldr r2, [r0]
add r0, r0, #4
ldr r3, [r0]
```

<sup>-</sup>Offsets increment by 4 because one word is 4 bytes

```
.data
arr:
  .word 32, 65, 76
  .text
                  r1: 32
ldr r0, = arr
ldr r1, [r0]
add r0, r0, #4
                  r2: 65
ldr r2, [r0]
add r0, r0, #4
                  r3: 76
ldr r3, [r0]
```

```
32
                                65
                                        76
  .data
arr:
                      arr | arr + 4 | arr + 8
  .word 32, 65, 76
  .text
ldr r0, = arr
                  r1: 32
ldr r1, [r0]
add r0, r0, #4
                  r2: 65
ldr r2, [r0]
add r0, r0, #4
                   r3: 76
ldr r3, [r0]
```

<sup>-</sup>Top right corner shows memory layout in terms of arr

#### Example:

register\_indirect.s

#### More on Memory Access

ldr r3, [r0]

- The above instruction uses the register indirect addressing mode
  - Addressing mode: how the processor accesses something
  - Register indirect: Memory access is done through an address in a register
- Many more available: see register\_\*.s

#### Array Access Example:

print array fixed length.s

## Writing to Array Example:

write\_array\_increasing.s

#### Another Array Access Example:

print array variable length.s