COMP 122/L: Computer Arch. and Assembly Language Summer 2023

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Course Description (from the catalog)
Introduction to computer architecture, assembly language programming system software and computer applications. Number systems and data representation. Internal organization of a computer. Primitive instructions and operations. Assembly language.

Learning Objectives
A successful student will learn basic assembly programming skills, understand the fundamentals of digital logic design, and understand the interface between the two. The MIPS instruction set will be used for assembly, though the concepts are broadly applicable. In particular, successful students will be able to:
- Describe how modern computers represent numbers, and interconvert between different numeric representations
- Perform common operations over computer-represented numbers, and design circuits which perform these operations
- Write programs in MIPS assembly, including conditionals, loops, arrays, and functions
- Design and simplify combinatorial circuits with Boolean algebra and Karnaugh maps (K-maps)
- Design sequential circuits, including those implementing finite state machines
- Understand, from a high-level, the main components of a high-level processor
- ...among many others

Course Motivation and Goal
A program doesn't have much utility until it runs on some piece of hardware. Here, we look at the low-level details behind execution, specifically the interface between hardware and software. Knowledge of these details can help you understand performance issues in your code, as well as writing low-level code like embedded software or operating systems. Ultimately, the goal of this class is to give you a basic working knowledge of these low-level details.

Textbook
No textbook is required. If you'd like a textbook for further study, two decent supplemental textbooks are:
- Computer Organization and Design: The Hardware/Software Interface (David A. Patterson and John L. Hennessy); any edition from the past several years
- Computer Systems Organization and Architecture (John D. Carpinelli)

Grading
You will receive a **single combined grade** for the lecture and lab. Your grade is based on the following components:

Lab Assignments	30%
Midterm #1	20%
Midterm #2	20%
Final Exam	30%

There will be roughly two lab assignments each week. The exact number of lab assignments has not been set, as this will depend somewhat on how the class progresses. Lab assignments are submitted through Canvas (https://canvas.csun.edu/). In the event that there is a problem with Canvas, you may email your assignment to me (kyle.dewey@csun.edu), though this should be considered a last resort.

Plus/minus grading is used, according to the scale below. The left column shows the minimal score necessary to receive the grade in the right column. The highest letter grade possible given the score is chosen; e.g., if you receive an 88.2, you'd receive a 'B+' for the course, which corresponds to being >= 86.5.

If your score is >=...	...you will receive...
96.5	A+
92.5	A
89.5	A-
86.5	B+
82.5	B
79.5	B-
76.5	C+
72.5	C
69.5	C-
66.5	D+
62.5	D
59.5	D-

If your score is >=...	...you will receive...
0	F

Collaboration for Assignments

All students are required to submit their own individual work. For assignments (and **only** assignments), students may discuss among each other, as long as they don't digitally share code. That is, you **cannot** simply email your code to someone else. However, you **may** discuss your actual code with someone else, including merely viewing code. The only stipulation is that **if you do discuss with someone else, say so in your submission.** This is not for punitive reasons; this is only so I get a sense of who is working with who. My intention with this policy is to enable collaborative learning, as opposed to simply sharing a solution.

Plagiarism and Academic Honesty

While collaboration is allowed on assignments, you are responsible for all of your own work. You may **not** take code from online sources and submit it as your own. If you must take code from online, cite where you took the code from. Worst-case scenario, you'll receive a 0 for whatever you took, but no further action will be taken. In general, code taken online which solves more general things (e.g., "how do I iterate through an array in Java") is more acceptable than code which solves more specific things (e.g., "how do I implement a recursive find function over immutable linked lists in Swift"). General bits of code only give you pieces of a solution, whereas specific bits of code often will give you a complete copy/pastable solution. If it's not 100% clear if something is permitted to be used or not, you can always ask me beforehand.

Chegg is specifically disallowed as an online resource, as it's almost always used as a repository of complete questions with answers. That is, the questions/answers are practically always of the specific kind mentioned above.

No discussion whatsoever is allowed during exams, except with the instructor. Any violations can result in a failing grade for the assignment/exam, or potentially failing the course for egregious cases. A report will also be made to the Dean of Academic Affairs. Students who repeatedly violate this policy across multiple courses may be suspended or even expelled.

Attendance

Attendance is not mandatory, and I won't be taking attendance. If you personally don't find the lecture / lab helpful, then you're under no obligation to attend. However, it's strongly encouraged, and historically students who attend these activities usually do better in the course overall.

Communication
In general, any questions should be made through Canvas. You can also email me, though I'm usually much faster to respond to Canvas than my general email.

Late Policy / Exam Scheduling
Late assignments will be accepted without penalty if prior arrangements have been made or there is some sort of legitimate emergency (at my discretion). If you must be absent from an exam, contact me ASAP to see if alternative accommodations can be made.

If an assignment is otherwise submitted late, it will be penalized according to the following scale:

If your assignment is late by <= this many days...	...it will be deducted by...
1	10%
2	30%
3	60%
4+	100%

To be clear, assignments which are submitted four or more days beyond the deadline will not receive credit. The reason for such a harsh late policy is that we will generally discuss solutions in class shortly after the deadline, and this late policy discourages people from simply pulling a solution from an in-class discussion.

Class Feedback
I am open to any questions / comments / concerns / complaints you have about the class. If there is something relevant you want covered, I can push to make this happen. I operate off of your feedback, and no feedback tells me "everything is ok".

Class Schedule (Subject to Change):

Week	Day	Content
1	Tuesday, 5/30	Syllabus, number representation
	Wednesday, 5/31	Binary operations
	Thursday, 6/1	Binary operations
2	Monday, 6/5	Floating point representation
	Tuesday, 6/6	Floating point representation
	Wednesday, 6/7	General assembly and MIPS introduction

Week	Day	Content
	Thursday, 6/8	MIPS arithmetic
3	**Monday, 6/12**	MIPS branches / conditionals / loops
	Tuesday, 6/13	MIPS branches / conditionals / loops
	Wednesday, 6/14	Midterm #1 Review
	Thursday, 6/15	**Midterm #1**
4	**Monday, 6/19**	MIPS memory / arrays
	Tuesday, 6/20	MIPS memory / arrays
	Wednesday, 6/21	Call stack / calling convention / MIPS functions
	Thursday, 6/22	Call stack / calling convention / MIPS functions
5	**Monday, 6/26**	Boolean logic and combinatorial circuits
	Tuesday, 6/27	Boolean logic and combinatorial circuits / simplifying circuits with Boolean algebra and K-maps
	Wednesday, 6/28	Simplifying circuits with Boolean algebra and K-maps / Midterm #2 Review
	Thursday, 6/29	**Midterm #2**
6	**Monday, 7/3**	Sequential circuits
	~~**Tuesday, 7/4**~~	No class (Independence Day)
	Wednesday, 7/5	Sequential circuits
	Thursday, 7/6	Finite state machines
7	**Monday, 7/10**	Finite state machines / Final Exam Review
	Tuesday, 7/11	**Final Exam**